

Z8™-03 MPE Microcomputer Protopack™ Emulator



Product Preview

Preliminary

September 1979

General Information

The Z8-03 MPE (Z8 PROTOPACK) is used for prototype development and preproduction of mask-programmed Z8-based applications. The Z8 Protopack is the ROM-less version of the standard Z8, housed in a pin-compatible 40-pin package.

To provide pin compatibility and interchangeability with the standard mask programmed Z8, the Z8 Protopack carries (piggy-backs) a 24-pin socket for a direct interface to program memory (Figure 1). The 24-pin socket is equipped with 12 ROM Address lines, 8 ROM Data lines and necessary control lines for various devices. Bonding options provided for this package allow the use of 2708, 2716, or 2732 EPROMs, as well as 82S2708, 82S190 Bipolar PROMs, for the first 2K × 8 bytes of program memory.

Pin compatibility allows the user to design the pc board for a final 40-pin mask-programmed Z8, and, at the same time, allows

the use of a Z8 Protopack to build the prototype and pilot production units. When the final program is established, the user can then switch over to the 40-pin mask programmed Z8 for large volume production. The new Z8 Protopack is also useful in small volume applications where masked ROM setup time, mask charges, etc., are prohibitive and program flexibility is desired.

Compared to the conventional EPROM versions of the single-chip microcomputers, the Z8 Protopack approach offers three main advantages:

- Flexibility in the use of EPROMs and Bipolar PROMs.
- Ease of developing various programs during the prototyping stage: For instance, in applications where the same hardware configuration is used with more than one program, the Z8 Protopack allows economical program storage in separate EPROMs (or PROMs), where the use of separate EPROM-based single-chip microcomputers is more costly.
- Elimination of long lead time in procuring EPROM-based microcomputers.

Highly adaptable to user needs, all versions of the Z8 offer high-speed instruction execution, efficient memory use, sophisticated interrupt and bit-manipulation capabilities, and system expandability. The device operates from a single +5 V supply. The standard 40-pin Z8 offers 2K × 8 ROM, 144 × 8 RAM, 32 I/O lines, two counter/timers and one serial I/O port.

Factory samples of the Z8-03 MPE Prototype device will be available in October 1979. For more information on ordering, contact:

Janak Pathak
ZILOG
10460 Bubb Road
Cupertino, CA 95014
(408) 446-4666

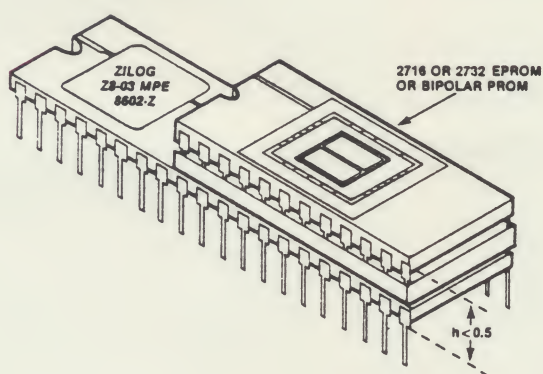


Figure 1. The Z8 Microcomputer Protopack Emulator

This is a preliminary specification. The information contained herein is subject to change.
Copyright © 1979 by Zilog, Inc.
Zilog assumes no responsibility for the use of any circuitry other than circuitry embodied in a Zilog product. No other circuit patent licenses are implied.